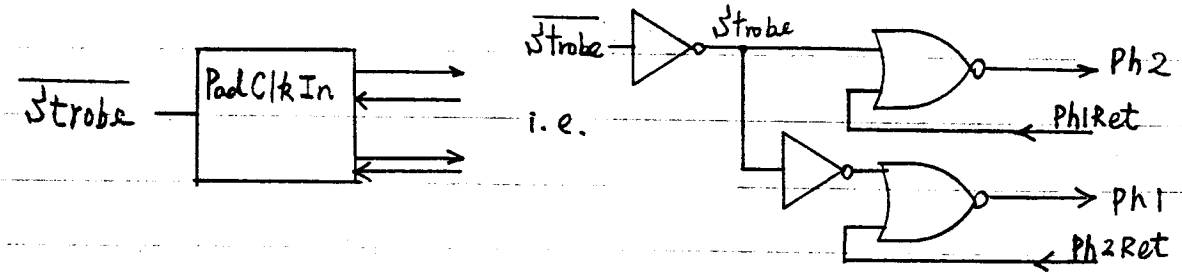
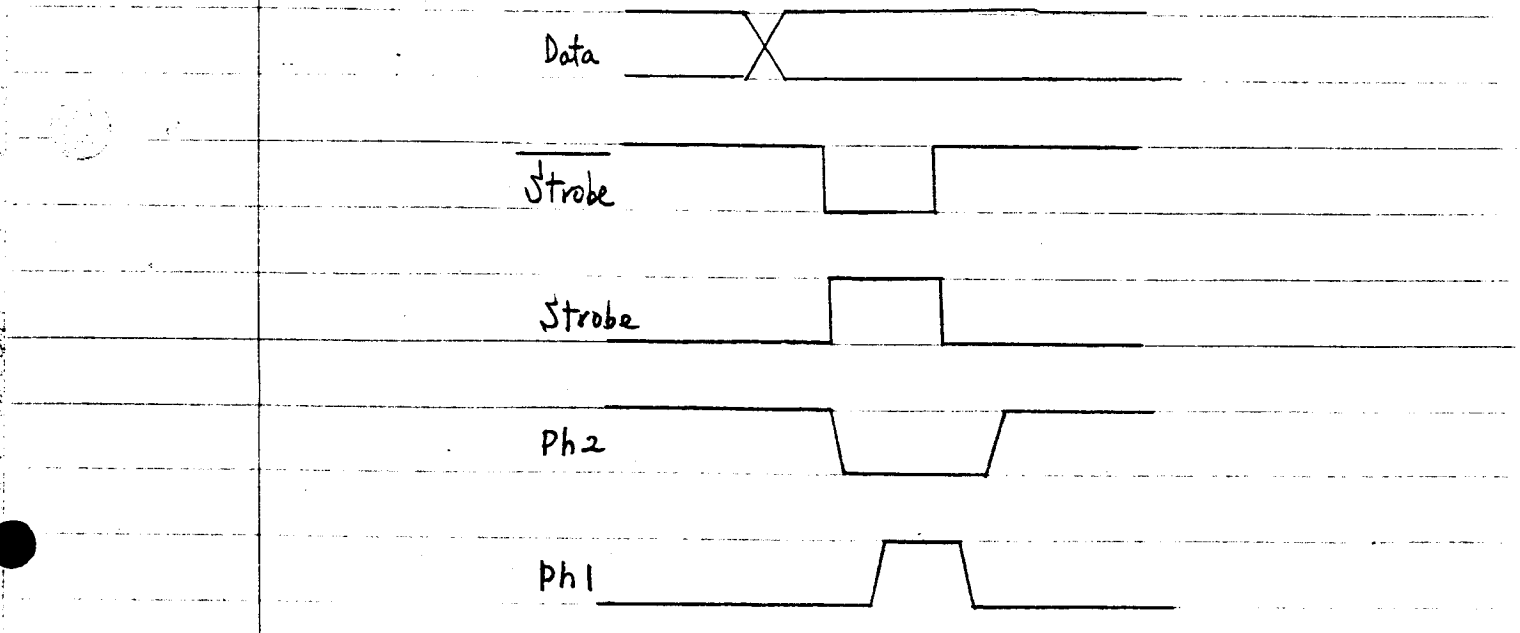
talk with JP, 5/23/85

* Use strobe to generate 2-phase clocks instead of using a ring oscillator



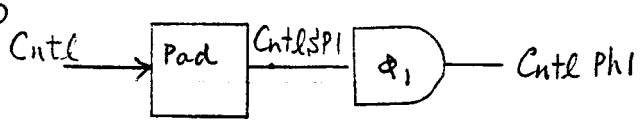
— Strobe ↔ Ph1 because data is stable during Ph1
— ∴ Ph2 mostly ON
— Input signals should be qualified at Ph1

* timing diagram

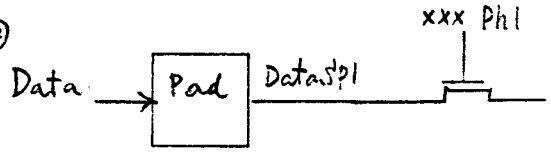


5/23/88

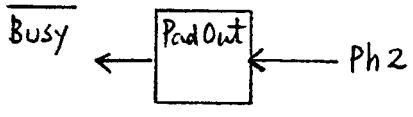
– Input / Output Interface

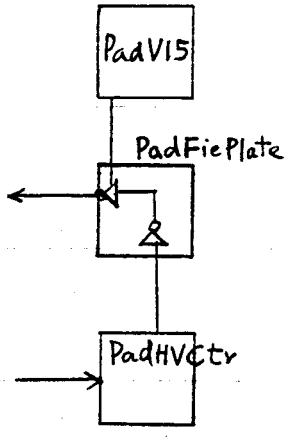
① 

where, Cntl: MWrt, LdEn, A&En, Trst.

② 

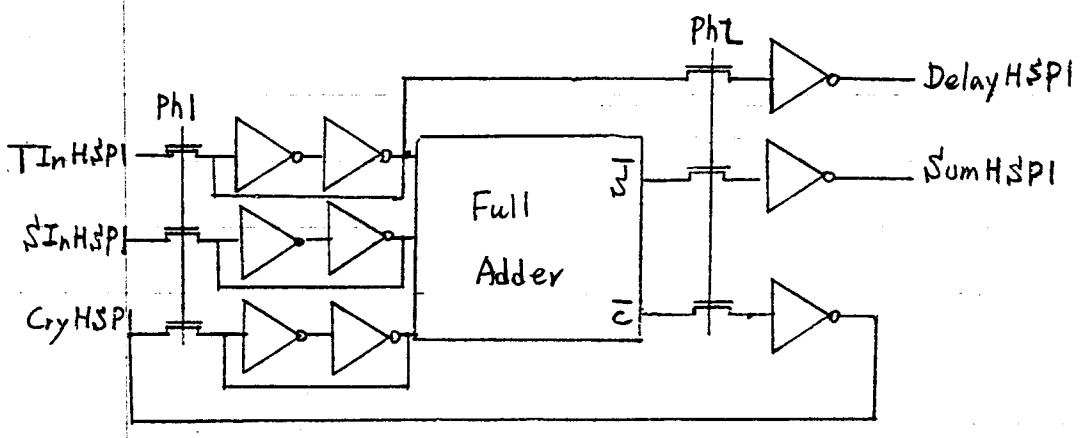
where, Data: ADat, BDat, CDat

③ 

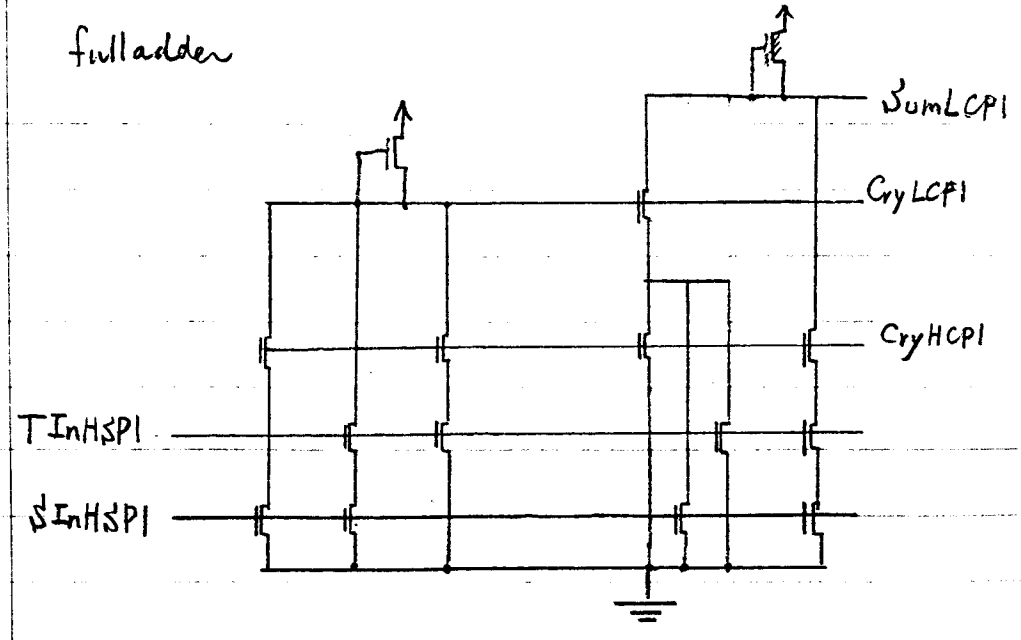
④ 

5/23/85

* Tree Adder Cell (jp, hch)



Note: the memory cells before the adder are to hold the data of CryHSPIi.

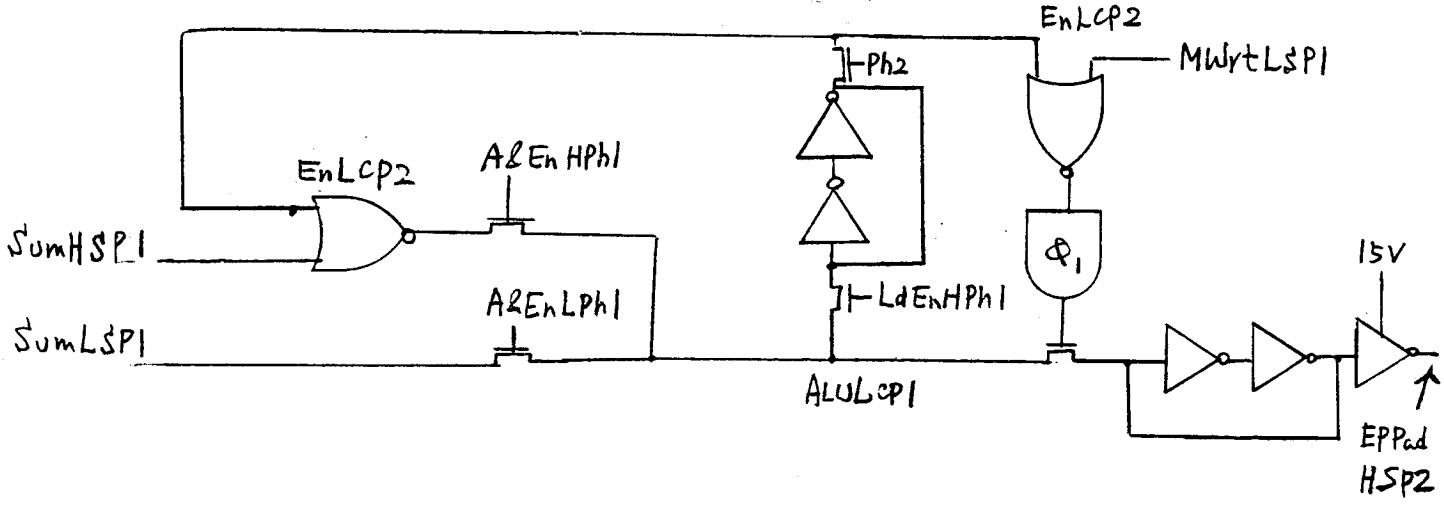


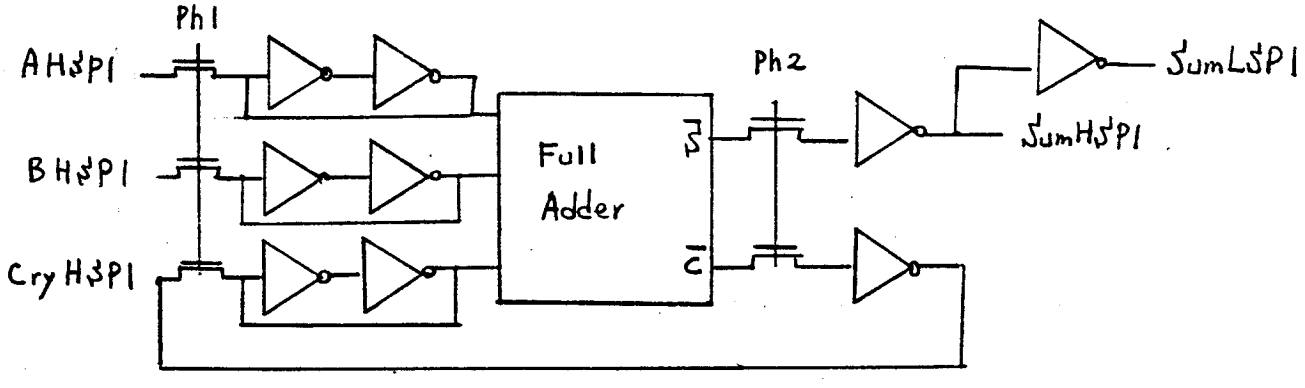
Pxpl Cell (hf, jp, hch)

Sum	En	ALU
1	x	0
0	1	1
0	0	0

$\therefore ALU = \overline{SUM} \cdot En = \overline{SUM + \overline{En}}$

i.e. During scan conversion, when sum = 1 then the pixel is disabled.





EP-8XPL jge/21-MAY-85

Centronics printer IF on TI 5-80

hold pin 21 = 0
hold pin 25 = 1 (evidently not necessary)

data appears valid on PD0-PD7 14ms
before STROBE ↓
and STROBE stays low 10 ms
data seems to remain valid for a long
time after STROBE ↑ (at least 2 ms)

writes at least 2ms apart
seem to occur about 4 ms apart

proposed format for word

D7	D6	D5	D4	D3	D2	D1	D0
HVCL	MWrt	Trst	ALUE En	LdEn	A	B	C

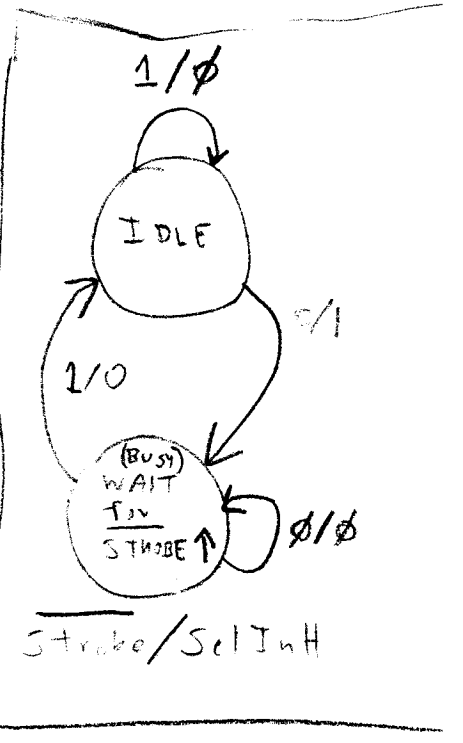
from BASIC, TI messes up if data is
0x09 - which is tab, sends 8 spaces inste
0x1A - which is ctrl-z, just skips it

For EP-8X8L

jge/22-MAY-85

~~NOT IDLE STB~~

IDLE	STB	IDLE(t+1)	SelIn
∅	X	1	∅
1	1	1	∅
1	∅	∅	1



$Busy_{i+1} = In(\overline{STB})$

$SelInHSP1 = Nor(Busy_i, \overline{STB})$

Busy	STB	$Busy_{i+1}$	SelInHSP1
1	∅	1	∅
∅	1	∅	∅
∅	∅	1	1
1	1	∅	∅