

arbitrary conic section

$$\left\{ Ax^2 + By^2 + Cxy + Dx + Ey + F = 0 \right\}$$

class of conic has nothing to do with value of $D + E$

3 classes: ellipse, hyperbola, parabola

parabola iff $4AB = C^2$

Memory to Memory Add - (others can be space/speed traded too)

speed optimized:
(minimum count = 2)

7 words
1 + 3 * count cycles

loop {

```

src+ cnt ; fetch S0
dst ain:φ bin:rddat ; fetch D0, save S0
src+ ain:sum bin:rddat CRY ; form S0, fetch
WRT dst+ ain:φ bin:rddat cnt
dst ain:sum
src+ ain:sum bin:rddat cin:crv CRY TC:-2
WRT dst+ done
    
```

space optimized:
(min. count = 1)

6 words
3 + 3 * count cycles

```

src+ CRY ; clear carry reg., fetch S0
bin:rddat ain:φ ; fetch D0, save S0
dst ain:sum cnt
src+ ain:sum bin:rddat cin:crv CRY
WRT dst+ ain:φ bin:rddat TC:-2
done
    
```

speed optimized mem from mem subtract Dst - Src → Dst

```

src+ cnt
dst ain:φ bin:rddatbar
src+ ain:sum bin:rddat cin:1 CRY
WRT dst+ ain:φ bin:rddatbar cnt
dst ain:sum
src+ ain:sum bin:rddat cin:crv CRY TC:-2
WRT dst+ done
    
```

space optimized

```

src+ ain:1 bin:1 CRY
bin:rddatbar ain:φ
dst ain:sum cnt
src+ ain:sum bin:rddat cin:crv CRY
WRT dst+ ain:φ bin:rddatbar TC:-2
done
    
```

MICRO-TIMING FOR CIRCLE LOAD (clock)

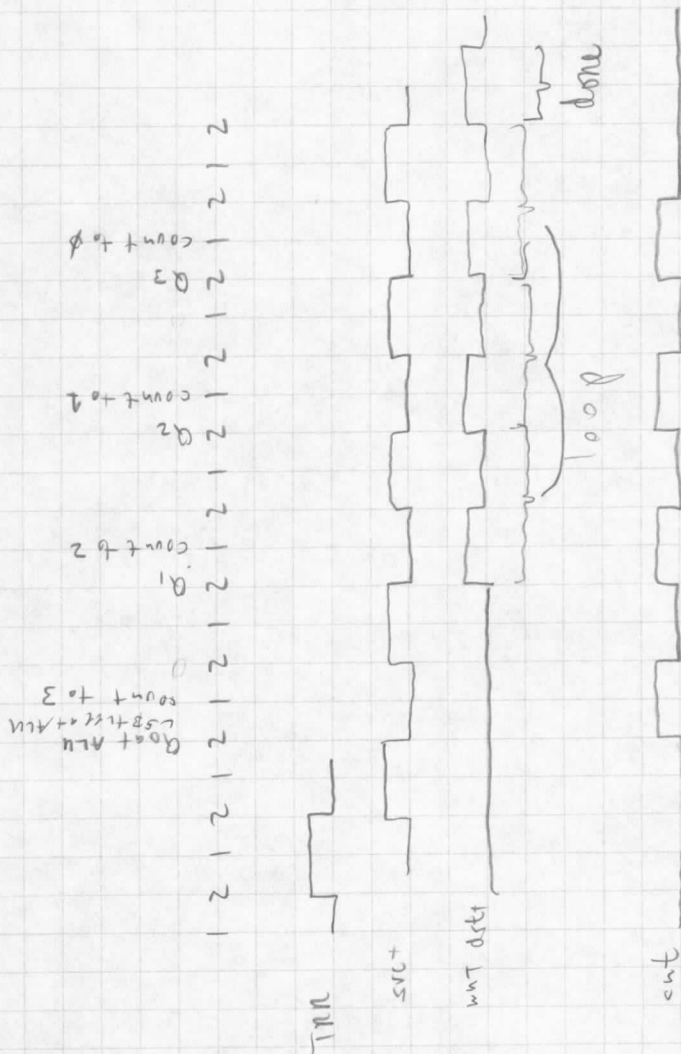
$STSrcAddr = \text{LSB (or approx. bit) of } Q\text{-buffer}$

$STDstAddr = \text{LSB of intensity buffer}$

$InstvCnt = \text{length of intensity buffer}$

$MBI = \emptyset$

$exp = DLEN - (NL + 4)$



PUSH-DEFINE

you define $A = 2x_c$, $B = 2y_c$, $C = r^2 - (x_c^2 + y_c^2)$
Q register = $x^2 + y^2$

for 512^2 system Q is 19 bits, unsigned

in ALU, compute $F = (A_x + B_y + C) - Q$

a pixel is enabled iff $F \geq 0$

specifically, add 1's complement of true result to
Q buffer, then AND "sum" of
result with ENABLE register

so $ain \leftarrow \text{truebin}$

$bin \leftarrow \text{rddat}$

and look at MSB of result

alternatively,

store $-(x^2 + y^2 + 1)$ in Q-buffer (same as 1's complement
of $x^2 + y^2$)

let $C = r^2 + 1 - (x_c^2 + y_c^2)$

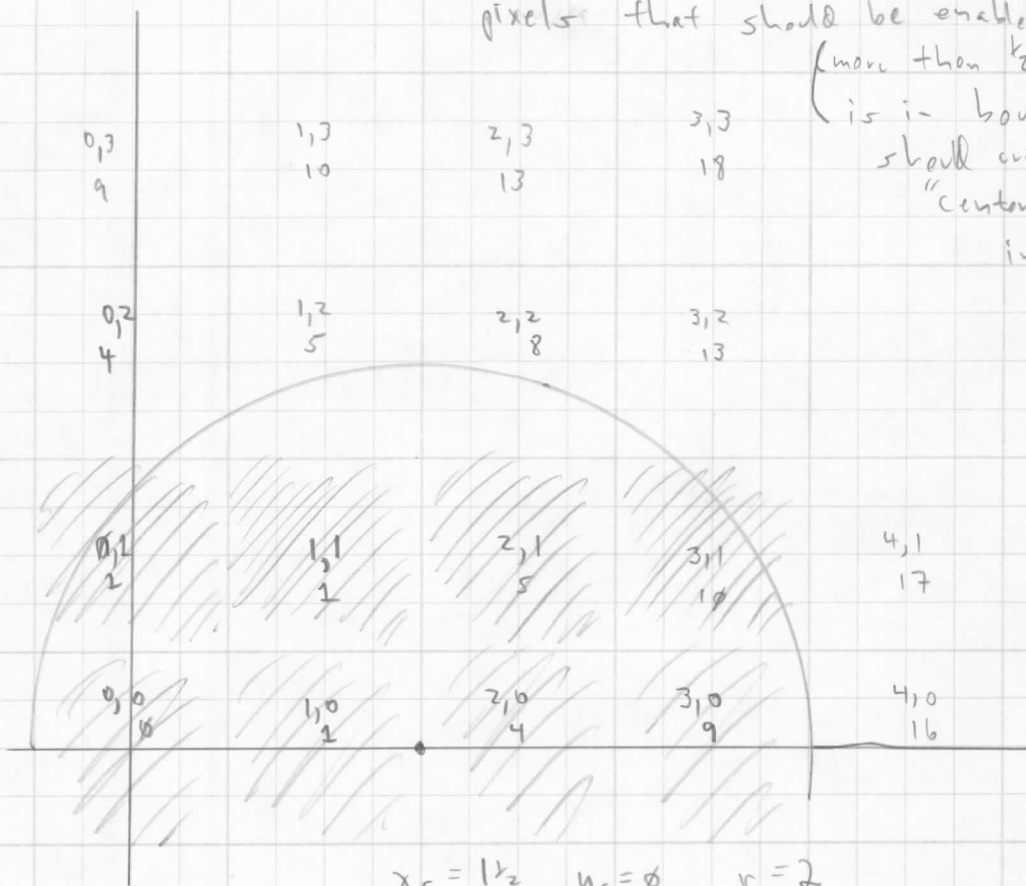
and add true result to Q-buffer [or add the
1 into C using carry bit at LSB

(although this would prevent using carry to
add 0.1 or $\frac{1}{2}$ roundoff into true result)

example of circle enable calculation

each big square represents a pixel,
labeled by its x, y coordinates
and $Q = x^2 + y^2$ value

pixels that should be enabled are shaded
(more than $\frac{1}{2}$ of area)
(is x -boundary
should criterion be
"center is in boundary
instead?)



$$\text{so } C = r^2 - (x_c^2 + y_c^2) = 1\frac{3}{4}$$

$$A = 2x_c = 3$$

$$B = 2y_c = 0$$

$$\text{inside if } Ax + By + C - Q \geq 0$$

$$\text{i.e.: } 3x + 1\frac{3}{4} \geq Q$$

so apparently you don't want to
add 0.5 to C or true result

Theory of Circle Shading

I_E, I_C are desired color intensities at edge and center of circle respectively
 i_e and i_c are the actual intensities obtained

n_p is the number of "passes" made

the algorithm first must select k_1, k_2, \dots, k_{n_p} such that

$$S = \sum_{i=1}^{n_p} 2^{k_i} \approx \frac{I_C - I_E}{r^2}$$

the accuracy will of course depend on n_p ; 2, 3, or 4 is probably a good number

load the Q buffer ($Q_{18} \dots Q_0$ for a 512×512 system) with $(x^2 + y^2)$, and provide additional bits, $Q_{-1}, Q_{-2}, \dots, Q_{-u}$ which are loaded with 0's

then, if we set

$$A = 2^{j+1} x_c$$
$$B = 2^{j+1} y_c$$
$$C = \begin{cases} 2^j [r^2 - (x_c^2 + y_c^2)] & j \neq 0 \\ r^2 - (x_c^2 + y_c^2) + I_C - S r^2 & j = 0 \end{cases}$$

and $5 \text{th} + 5 \text{th} \text{ Addr} = Q_{-j}$

then $Ax + By + C + Q = 2^j \{ r^2 - [(x-x_c)^2 + (y-y_c)^2] \}$
(for $j \neq 0$)

theory of circle shading

if we do this for $j = k_1, k_2, \dots, k_{n_p}$ and add the results, we obtain

$$S \{ r^2 - [(x-x_c)^2 + (y-y_c)^2] \} + I_c - S r^2$$

$$\text{so } \left. \begin{aligned} i_c &= I_c \\ i_e &= I_c - S r^2 \approx I_e \\ i_c - i_e &= S r^2 \end{aligned} \right\} \begin{array}{l} \text{intensity at center is} \\ \text{exact, intensity at} \\ \text{edge is approximate} \end{array}$$

practical limitations: in a 512×512 system, we need 19 bits to represent $x^2 + y^2$;

if we only use 20 bits for the Q-buffer, i.e.: $q = 1$, $Q-1 = \phi$, we "waste" 1 bit then $k_{n_p} \leq 1$, since the k_i are in ascending order of magnitude, hence $S \leq 2^{-19}$

i.e.: if $n_p = 3$, $S \leq 3^{1/2}$, so $i_c - i_e \leq 3^{1/2} r^2$
 so for 8 bit intensity buffers, the smallest circle for which intensity could go from 0 to max from edge to center would have radius 9
 additionally, the constraint $k_1 \geq -12$ would have to be imposed,

THEORY: MBR TAKEW REGISTER + MBR SHIFTER

if length A (not including signbit) = \bar{A}
 length B (") = \bar{B}
 length C (") = \bar{C}

and the tree has NL levels,
 then the result may be as long as $2 + \max(\bar{A} + NL, \bar{B} + NL, \bar{C})$
 bits long, not including signbit

so if MBI is 1, the most significant bit^(signbit) of the tree
 result is important, then the coefficients must
 be sign extended to

$$N = \max(\bar{A} + NL, \bar{B} + NL, \bar{C}) + 3 \text{ bits}$$

so a 1 must be placed in the MBR shifter
 at index $N-1$ (where rightmost bit is \emptyset)
 these coefficient lengths include frabits

9+fbits

note that the length of a coefficient, not including signbit,
 is $\overline{COEF} = (\# \text{ frabits}) + (\text{exp}) + 1$
 wher exp is the IEEE format exponent

assuming shifter indexes match, takes register indexes
 for A + B place a 1 in taken register at
 $\text{index} = \text{fbits} + \text{exp}(A \text{ or } B) + NL + 3$

for C place a 1 in taken register at
 $\text{index} = \text{fbits} + \text{exp}C + 3$

[the number of bits of a coefficient that get shifted out
 is (index of leftmost 1) + 1]

if instead, we place for C a 1
 at index = $fbits + expC + NL + 3$
 (analogously to A & B coefficients)

then we waste clock cycles whenever

$$\bar{C} < \max(\bar{A}, \bar{B}) + NL$$

$$\text{and } MBI = 1$$

so we don't waste clock cycles on planar loads

where $A=B=0$, because $MBI = \emptyset$ and

A, B, C coefficient tokens are cleared on IAEH

For the exponent field of the I word, there are several things we may want to do:

1) for an instruction with no coeffs, set $expfield = 255$
 and $MBI = 0$; the MBR shifter will
 be loaded with all 0's

2) for an instruction for which only the first FN
 bits of the tree result are important, only
 the first FN bits of the coefficients need
 be transmitted; set $MBI = \emptyset$ to clear the
 tokens loaded for the coeffs, and place a
 1 in the MBR shifter at

$$index = FN - 1$$

this would be done by setting $expfield = 12$

$$expfield = 127 + FN' - (NL + 4) \text{ where } FN' = FN - FN_i$$

3) For an instruction for which the MSB (signbit) of the tree result is important, MBI=1, but the result need have no minimum length, i.e. edges, stripes, we must insure that MBR occurs no earlier

than $\xrightarrow{\text{at least}} 2$ clock cycles after TRR since TRR occurs 2 cycles before the LS wholebit of result appears at ALU and MBR occurs 1 cycle before signbit appear at ALU,

i.e. signbit arrives at tree 1 cycle after LS wholebit arrives we want to protect that result can be $2 + fbits$ long including signbit

so should place 1 in MSB token register at index = $fbits + 1$

so $expfield = 127 - (NL + 2)$

so a cycle is wasted if $fbits < NL + 1$

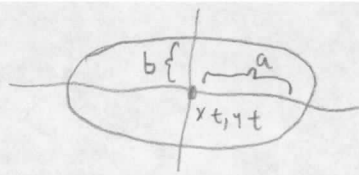
4) For an instruction for which the MSB (signbit) of tree result is important, but result must be sign-extended to FX bits in any case (such as dist-define), set MBI=1, and; we want to insure the MBR occurs no earlier than 1 cycle after ICT2H

individual cases must be handled differently for dist define, MBR must occur no earlier than $QLEN + 2$ microcycles after TRR

place 1 in token reg. at index = $fbits + QLEN + 1$

$expfield = 127 + QLEN - (NL + 2)$

Drawing Ellipses



$$E = a^2 x_r^2 + b^2 (1 - x_r^2)$$

$$F = a^2 (1 - x_r^2) + b^2 x_r^2$$

$$G = 2 x_r \sqrt{1 - x_r^2} (b^2 - a^2)$$

x_r refers to
rotation about
origin

$$\left\{ \begin{array}{l} A = 2 x_t F + y_t G \\ B = 2 y_t E + x_t G \\ C = a^2 b^2 - (x_t^2 F + x_t y_t G + y_t^2 E) \\ Q = F x^2 + G xy + E y^2 \end{array} \right. \quad \cdot \quad x_t, y_t \text{ is center}$$

$$\text{within ellipse} \equiv [A x + B y + C - Q \geq 0]$$

equivalent to within ellipse \equiv

$$F(x - x_t)^2 + G(x - x_t)(y - y_t) + E(y - y_t)^2 \leq a^2 b^2$$

$$\left\{ \begin{array}{l} \text{with } a \text{ along } x\text{-axis, } b \text{ along } y\text{-axis:} \\ E = a^2, F = b^2, G = 0 \\ A = 2 x_t b^2, B = 2 y_t a^2, C = a^2 b^2 - x_t^2 b^2 - y_t^2 a^2 \\ Q = b^2 x^2 + a^2 y^2 \end{array} \right.$$

test example of ellipse

$$\begin{aligned} \text{let } X_t = Y_t &= 15 && (\text{center at } 15, 15) \\ X_r &= \sqrt{2}/2 && (\text{rotated } 45^\circ \text{ to normal}) \\ a &= 10, b = 6 && (\text{bounding box } 20 \times 12) \end{aligned}$$

$$\text{then } E = 68, F = 68, G = -64$$

$$A = 30 \cdot 68 - 15 \cdot 64 = 1080$$

$$B = 30 \cdot 68 - 15 \cdot 64 = 1080$$

$$C = 3600 - (225 \cdot 68 + 225 \cdot 68 - 225 \cdot 64) = -12600$$

$$Q = 68x^2 - 64xy + 68y^2$$

scale everything by $1/4$

$$A = B = 270, C = -3150$$

$$\begin{aligned} Q &= 17x^2 - 16xy + 17y^2 \\ &= x(17x - 16y) + y(17y) \end{aligned}$$

implemented in `el2.in`

17 bits should suffice for Q-buffer